SMART
300mm Sorter datasheet

The **SMART** is our versatile solution for emerging technologies

SEMI300/S2/S14 compliant
ISO Class 1 mini-environment
Load-Ports SEMI compliant E15.1/E57/E62
Suits all SEMI compliant FOUP/FOSB carriers

MAIN SPECIFICATIONS

Type	SOR300S	SOR300M	SOR300L
Load-port configuration	2 Load-Ports	3 Load-Ports	4 Load-Ports
Substrate type(s) Diameter 300 +/- 0.2mm	▪ Si wafer ▪ Glass ▪ Stacked ▪ Mold Compound		
Throughputs[1] *Wafer per Hour*	▪ Without OCR: Up to 540 wph ▪ With OCR: Up to 305 wph		
Cleanliness[2]	Down to 0.003 at 26nm particle size		
Power Voltage	Single phase 230 Volts (+/- 10%) 50/60Hz		
Full load current	5A	6A	7A
Mini-environment	▪ U16 Ultra Low Penetration Air Filters ▪ ISO (14644-1) Class 1 mini-environment		
Wafer mapping	Double and cross-sloted wafer detection		
Carrier InfoPads	Highly configurable rules and behaviors		
HOST	Communication complies to SECS/GEM & GEM300 Standards		

[1] Mesurements done in local mode, after sorter's settings & adjustments properly completed
[2] Particle per Wafer Pass. Measured in an ISO Class 4 cleanroom environment (as per ISO 14644-1), on a sorter properly maintained and with clean substrates and carriers (initial PWP at less than 50 particles)

AVAILABLE OPTIONS

- FOUP Protections
- Ionizer
- Flip
- AMC Filtering
- E84
- Carrier RF-ID and/or Barcode reader
- Rotational Loading stage
- Duplex Wafer ID reading
- N2/XCDA FOUPs' Purge
- & more...

FEATURES & Benefits

- Various substrates & materials
- **Vacuum Free** *A.C.T*[1] backside or edge contact end-effectors
- Dual Handling option (end-effectors & notch alignment station)
- Accomodating Auto segregation
- Connection with OHT available
- 2nm application compliant

[1] Advanced Contact Technology

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CE Intertek S2-S8

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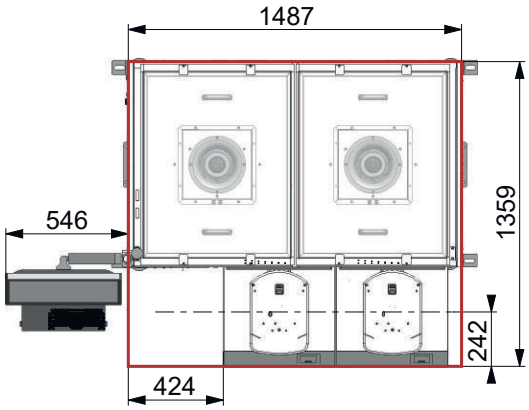
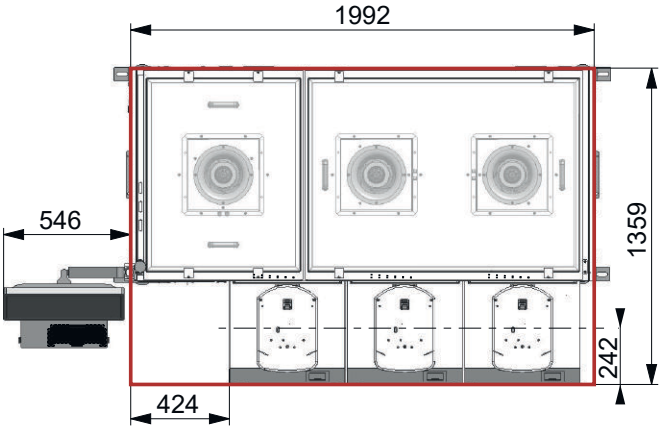
SORTERS LAYOUT *Width x Depth*

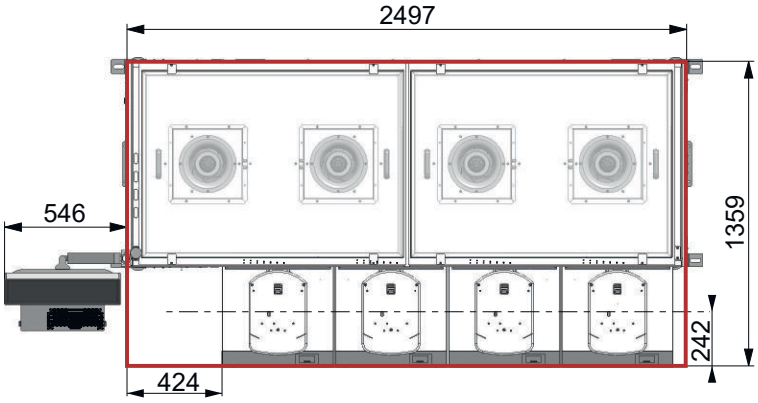
2 Load Ports
1.49x1.36m
2.02m² / 21.74ft²

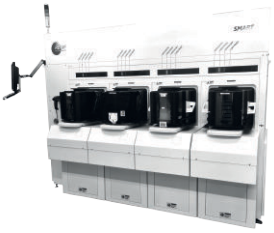


3 Load Ports
1.99x1.36m
2.71m² / 29.06ft²

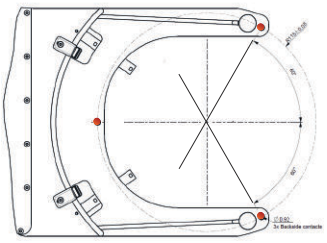


4 Load Ports
2.50x1.36m
3.39m² / 36.49ft²

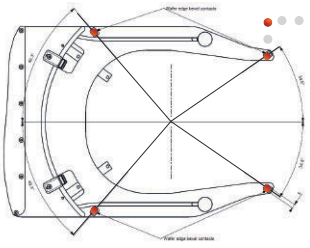




END-EFFECTORS LAYOUTS



Backside contact end-effector
Wafer contact area: **0.01%**



Edge contact end-effector
Wafer contact area: **0.003%**

Slight contact on
wafer edge bevel

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